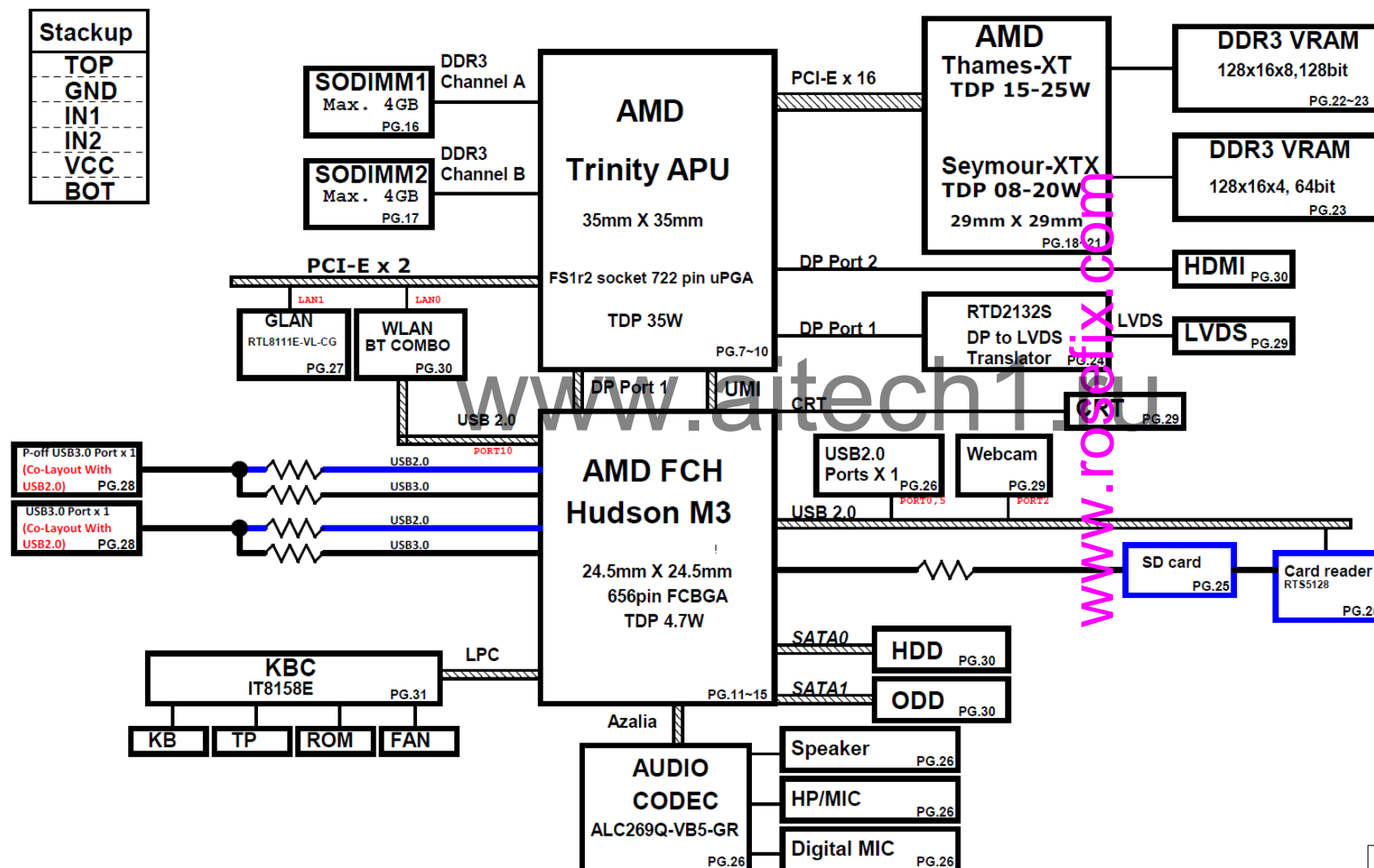


K45DR Repair Guide

01

XY1 AMD Comal UMA/Muxless SYSTEM DIAGRAM



Quanta Computer Inc.

PROJECT : XY1

Block Diagram

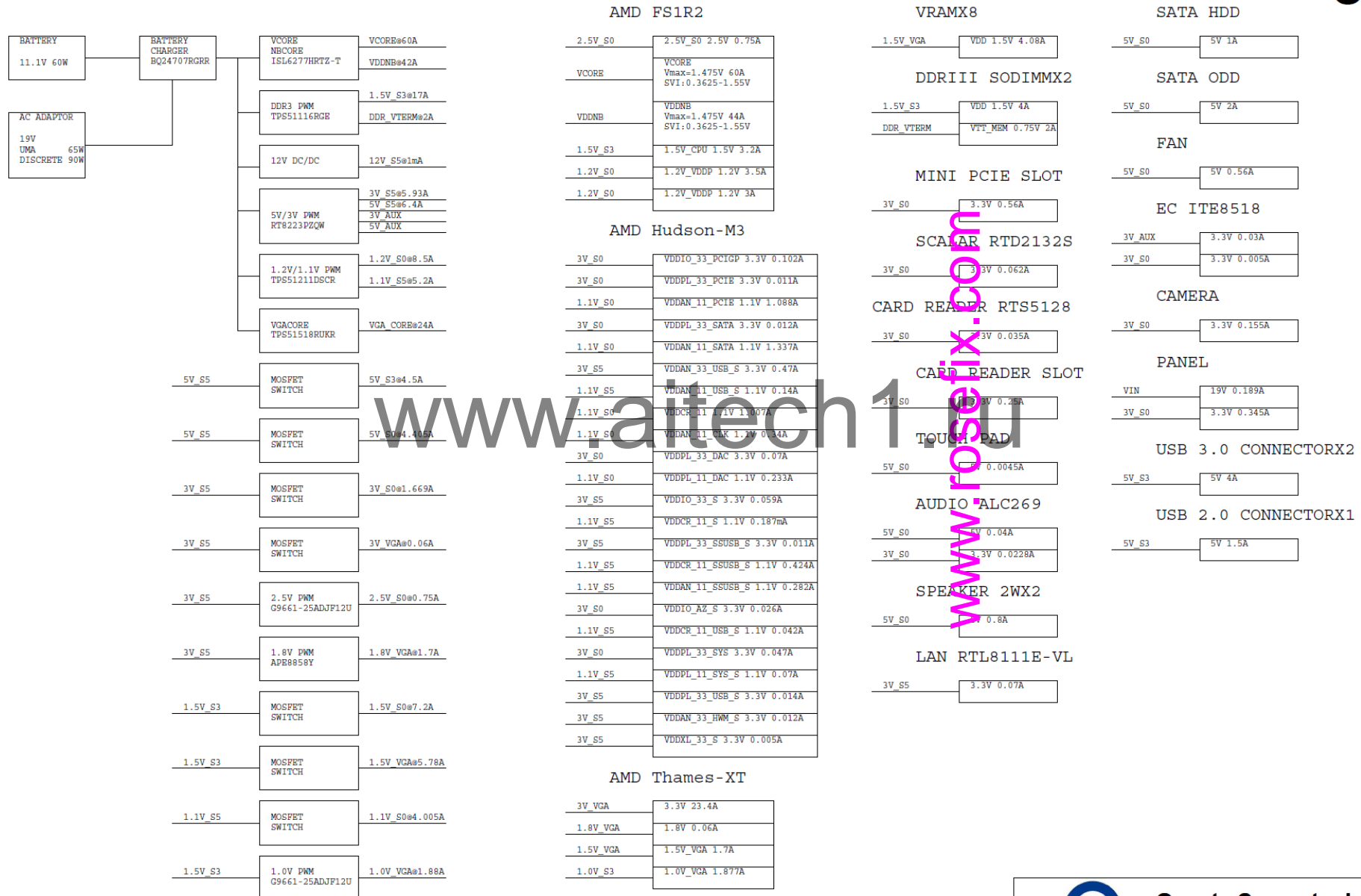
Size: Document Number: Rev: 1A

Date: Monday, February 06, 2012 Sheet 1 of 44

POWER FLOW

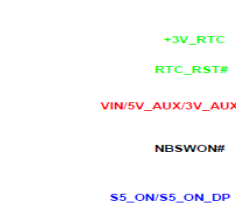
POWER DISTRIBUTION

03

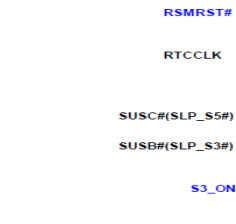


1.5V_S3/5V_S3
S0_ON_1/GPIO_ON_1/ODDPD
3V_VGA/5V_S0/3V_S0/2.5V_S0

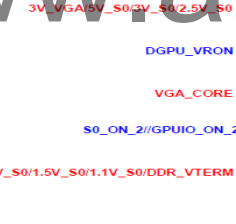
XY1 Power On/Off Sequencing Timing Diagram



3V_S5/5V_S5/3V_S5_DP/1.1V_S5



S0_ON_1/GPUIO_ON_1/ODD_PD



1.2V_S0/1.5V_S0/1.1V_S0/DDR_VTERM

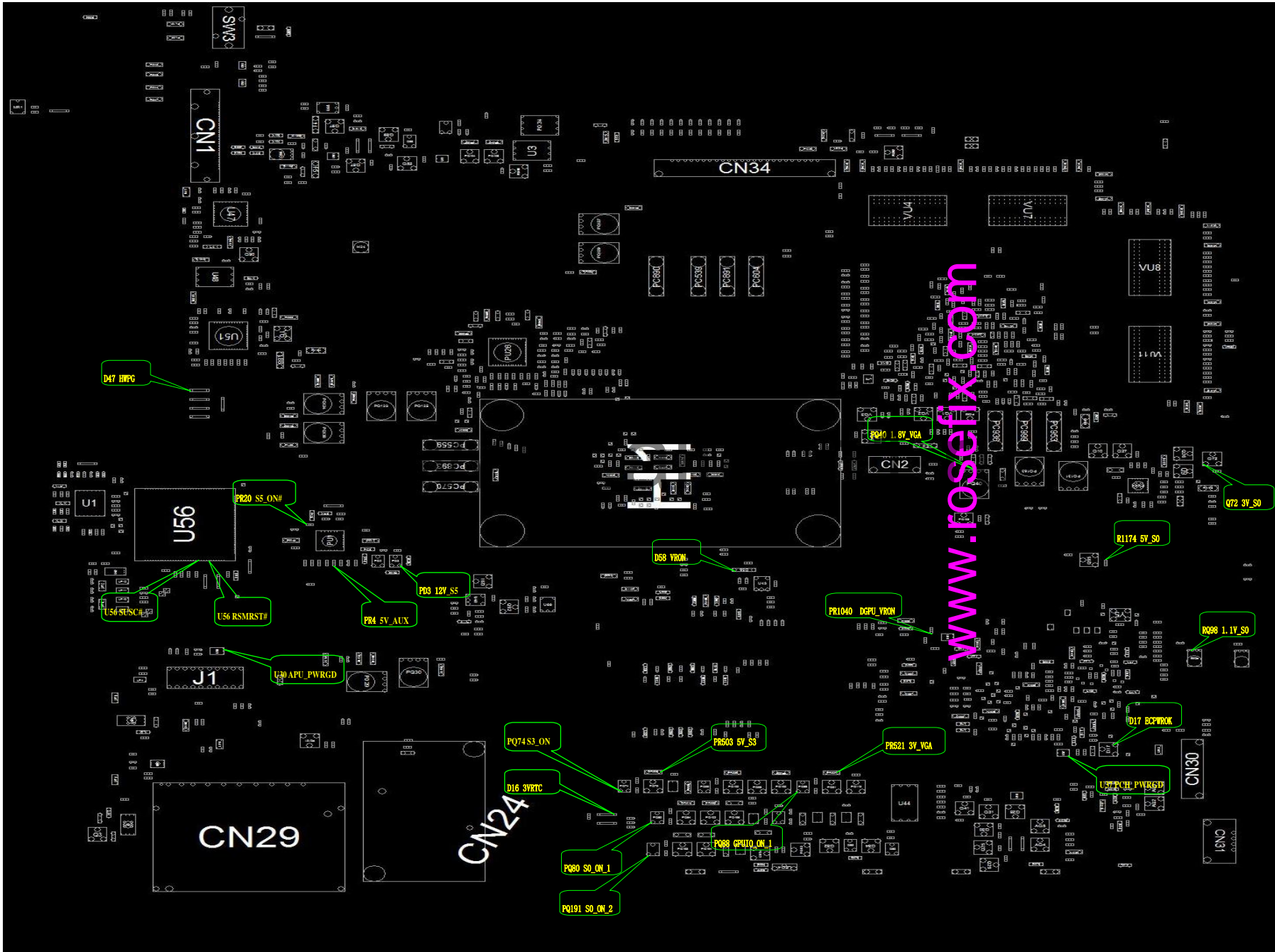
```

HWPG
VRON
VCC_CORE/VDDNB
ECPWROK
FCH_PWRGD
APU_PWRGD
PCIE_CLK
SIO_RCIN#
PLT_RST#
PCIE_RST#
CPU_LDT_RST#

```



Voltage & Signal Measure Point TOP



Voltage & Signal Measure Point Bottom

